IN THE CLAIMS

Please amend the claims as follows:

1. (original) A display comprising:
a first voltage supply line;
a second voltage supply line; and
a plurality of pixel cells, each pixel cell including a pixel electrode, a storage element for storing a data bit, and a switch responsive to said data bit and operative to selectively couple said pixel electrode with one of said first voltage supply line and said second voltage supply line.

2. (original) A display according to Claim 1, wherein said switches comprise multiplexers.

3. (original) A display according to Claim 1, further comprising:
a voltage controller including
a first voltage source coupled to assert a first predetermined voltage on said first voltage supply line, and
a second voltage source coupled to assert a second predetermined voltage on said second voltage supply line.

4. (original) A display according to Claim 3, wherein:
said display further comprises a common electrode; and
said voltage controller further comprises a third voltage source coupled to assert a third predetermined voltage on said common electrode.

5. (original) A display according to Claim 4, wherein said voltage controller, responsive to a control signal, is operative to assert a same one of said first predetermined voltage, said second predetermined voltage, and said third predetermined voltage on each of said first voltage supply line, said second voltage supply line, and said common electrode.

6. (original) A display according to Claim 4, wherein:

said voltage controller further comprises a fourth voltage source coupled to assert a fourth predetermined voltage on said common electrode;

responsive to a first control signal, said voltage controller asserts said first predetermined voltage on said first voltage supply line, said second predetermined voltage on said second voltage supply line, and said third predetermined voltage on said common electrode; and

responsive to a second control signal, said voltage controller asserts said second predetermined voltage on said first voltage supply line, said first predetermined voltage on said second voltage supply line, and said fourth predetermined voltage on said common electrode.

7. (original) A display according to Claim 4, wherein:

said voltage controller further comprises a fourth voltage source coupled to assert a fourth predetermined voltage on said first voltage supply line, and a fifth voltage source coupled to assert a fifth predetermined voltage on said second voltage supply line;

responsive to a first control signal, said voltage controller asserts said first predetermined voltage on said first voltage supply line, said second predetermined voltage on said second voltage supply line, and said third predetermined voltage on said common electrode; and

responsive to a second control signal, said voltage controller asserts said fourth predetermined voltage on said first voltage supply line, said fifth predetermined voltage on said second voltage supply line, and said third predetermined voltage on said common electrode.

8. (original) A display according to Claim 7, wherein:

said voltage controller further comprises a sixth voltage source coupled to assert a sixth predetermined voltage on said common electrode; and

responsive to said second control signal, said voltage controller asserts said sixth predetermined voltage on said common electrode.

9. (currently amended) A display comprising:

a pixel electrode;

a first voltage supply terminal;

a second voltage supply terminal;

a storage element including an output terminal for storing and outputting a data bit; and

a switch including a control terminal coupled to said output terminal of said storage element, a first input terminal coupled to said first voltage supply terminal, a second input terminal coupled to said second voltage supply terminal, and an output terminal coupled to said pixel electrode; and wherein

said switch asserts a voltage supplied by said first voltage supply terminal or a voltage supplied by said second voltage supply terminal on said output terminal responsive to a value of said data bit asserted on said control terminal.

10. (original) A display according to Claim 9, wherein said switch comprises a multiplexer.

11. (original) A display according to Claim 9, further comprising:

a voltage controller including

a first voltage source coupled to assert a first predetermined voltage on said first voltage supply terminal, and

a second voltage source coupled to assert a second predetermined voltage on said second voltage supply terminal.

12. (original) A display according to Claim 11, wherein:

said display further comprises a common electrode; and

said voltage controller further comprises a third voltage source coupled to assert a third predetermined voltage on said common electrode.

13. (original) A display according to Claim 12, wherein said voltage controller, responsive to a control signal, is operative to assert a same one of said first predetermined voltage, said second predetermined voltage, and said third predetermined voltage on each of said first voltage supply terminal, said second voltage supply terminal, and said common electrode.

14. (original) A display according to Claim 12, wherein:

said voltage controller further comprises a fourth voltage source coupled to assert a fourth predetermined voltage on said common electrode;

responsive to a first control signal, said voltage controller asserts said first predetermined voltage on said first voltage supply terminal, said second predetermined voltage on said second voltage supply terminal, and said third predetermined voltage on said common electrode; and

responsive to a second control signal, said voltage controller asserts said second predetermined voltage on said first voltage supply terminal, said first predetermined voltage on said second voltage supply terminal, and said fourth predetermined voltage on said common electrode.

15. (original) A display according to Claim 12, wherein:

said voltage controller further comprises a fourth voltage source coupled to assert a fourth predetermined voltage on said first voltage supply terminal, and a fifth voltage source coupled to assert a fifth predetermined voltage on said second voltage supply terminal;

responsive to a first control signal, said voltage controller asserts said first predetermined voltage on said first voltage supply terminal, said second predetermined voltage on said second voltage supply terminal, and said third predetermined voltage on said common electrode; and

responsive to a second control signal, said voltage controller asserts said fourth predetermined voltage on said first voltage supply terminal, said fifth predetermined voltage on said second voltage supply terminal, and said third predetermined voltage on said common electrode.

16. (original) A display according to Claim 15, wherein:

said voltage controller further comprises a sixth voltage source coupled to assert a sixth predetermined voltage on said common electrode; and

responsive to said second control signal, said voltage controller asserts said sixth predetermined voltage on said common electrode.

17. (currently amended) A display comprising:

first supply terminal means for receiving an asserted predetermined voltage;

second supply terminal means for receiving [[an]] a second asserted predetermined voltage;

storage means for storing a data bit; and

pixel means including a pixel electrode, said pixel means responsive to said data bit and operative to transmit [[an]] said asserted ~~voltage~~ predetermined voltages from one of said first supply terminal means and said second supply terminal means to said pixel electrode.

18. (currently amended) A display according to Claim 17, further comprising controller means for selectively asserting said predetermined voltages on said first supply terminal means and said second supply terminal means.

19. (currently amended) A display according to Claim 18, further comprising:

a common electrode; and

wherein said controller means is further operative to selectively assert other predetermined voltages on said common electrode.